

# FIG.1

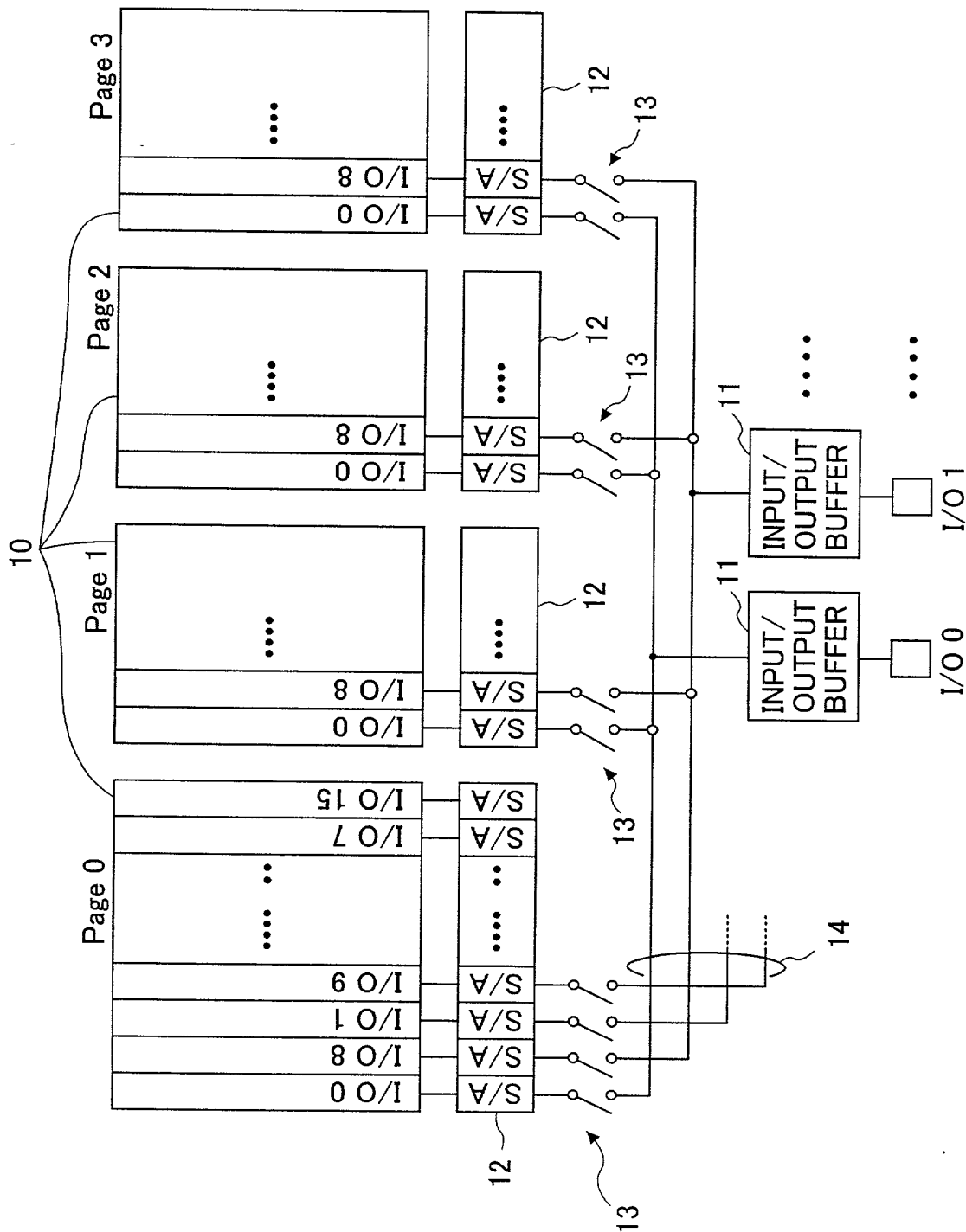


FIG.2

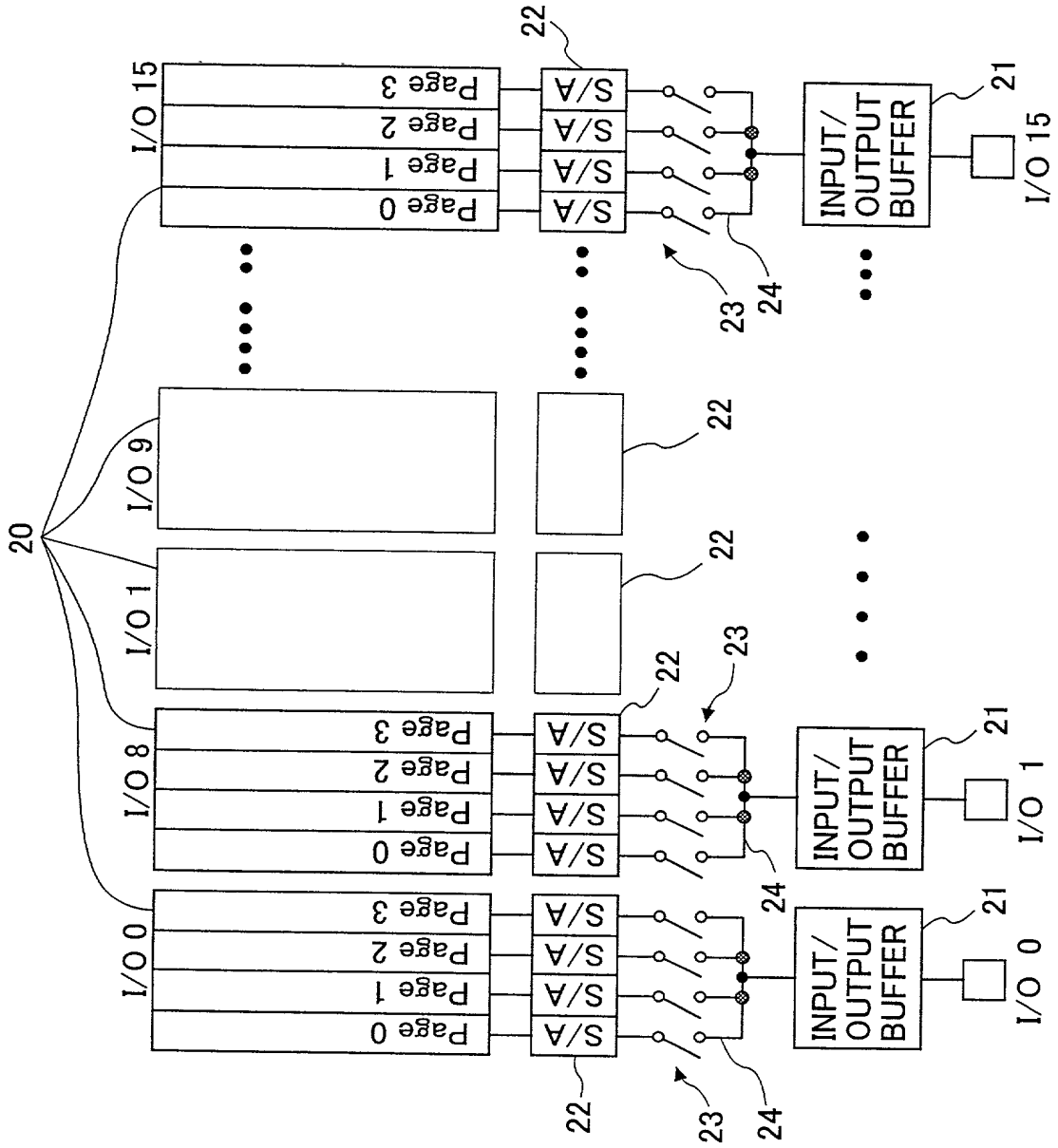
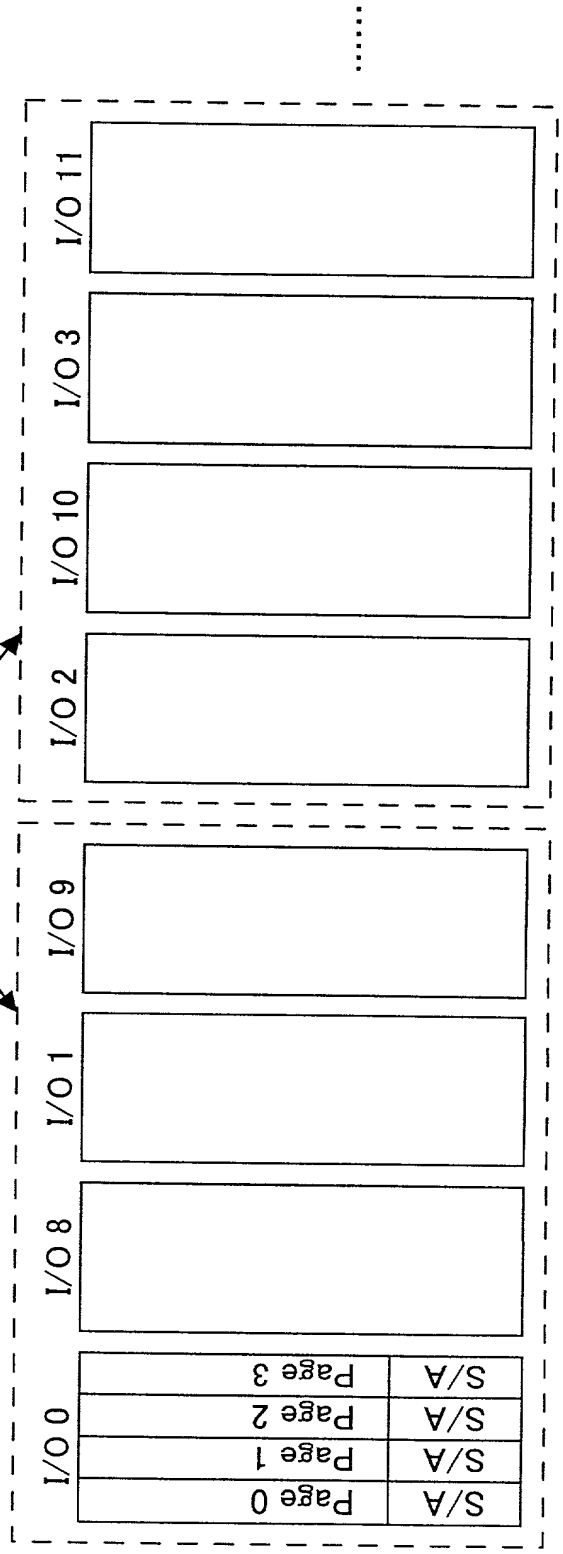


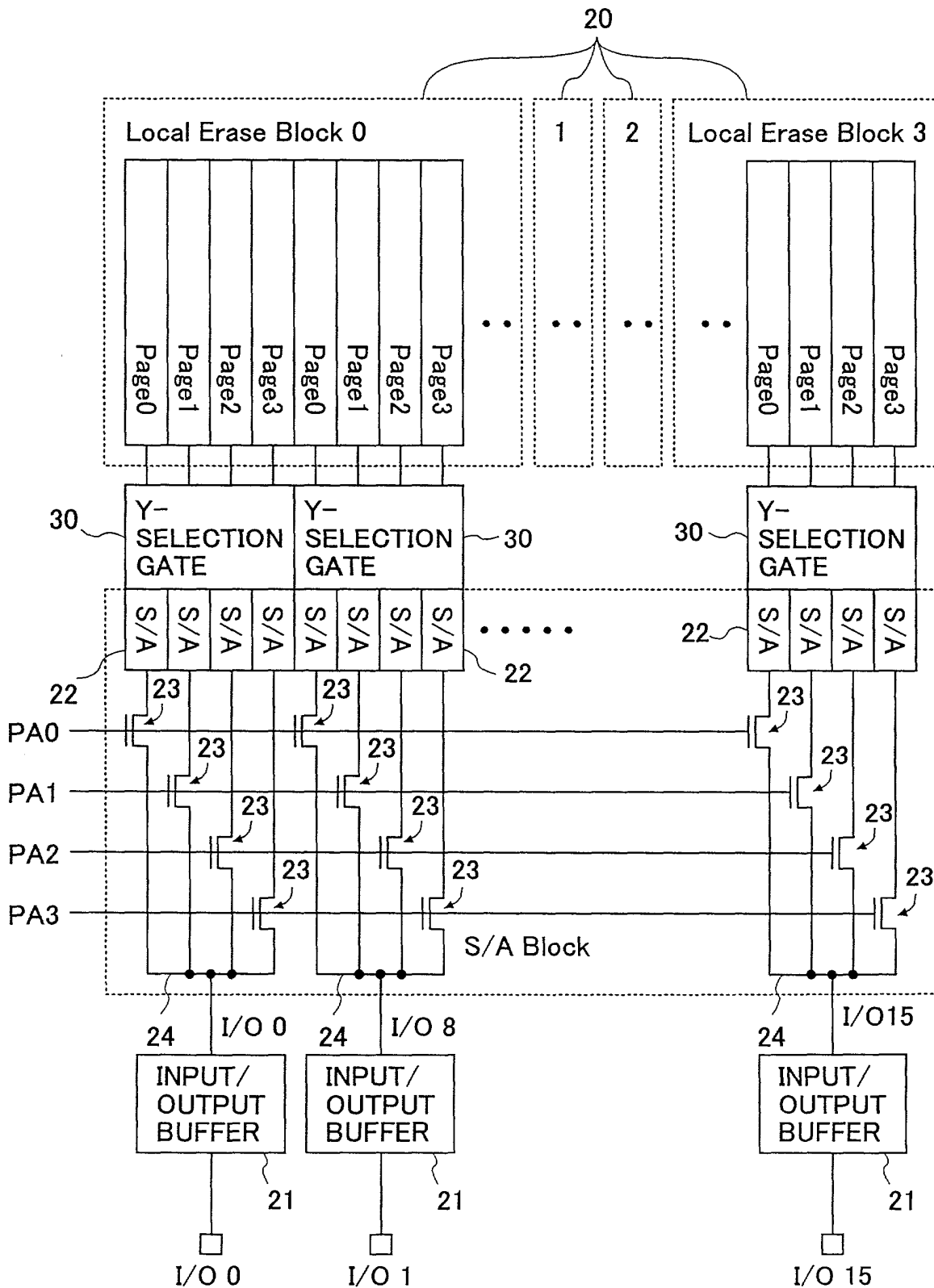
FIG. 3 is a block diagram of a unit of erasure (local erase block) according to an embodiment of the present invention. The unit of erasure is divided into a first region and a second region. The first region includes a first sub-region and a second sub-region. The first sub-region includes a first page and a second page. The second sub-region includes a third page and a fourth page. The second region includes a fifth page and a sixth page. The unit of erasure is used to store data and is used to erase data.

FIG.3

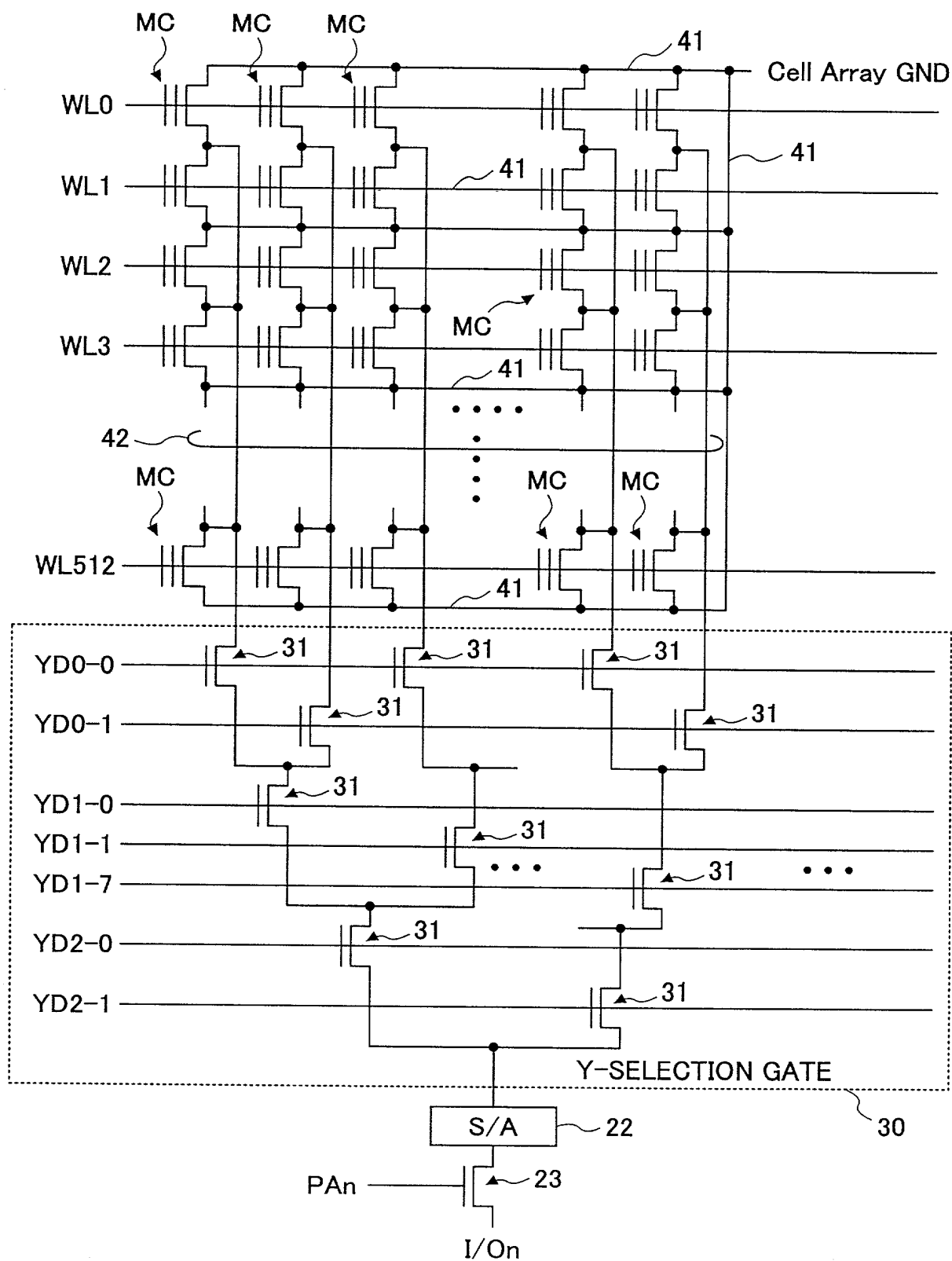
UNIT OF ERASURE (LOCAL ERASE BLOCK)



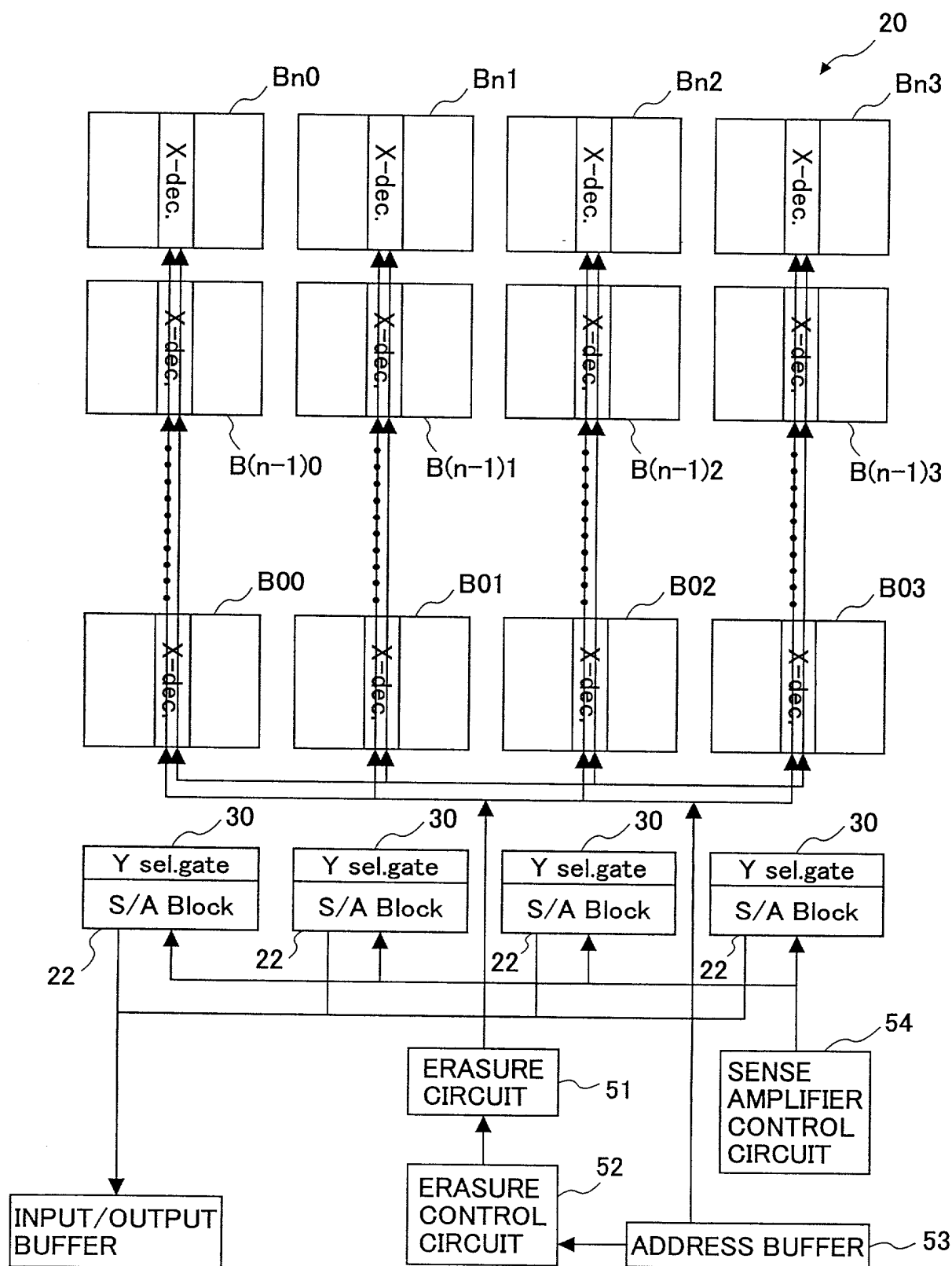
# FIG.4



# FIG.5



# FIG. 6



# FIG. 7

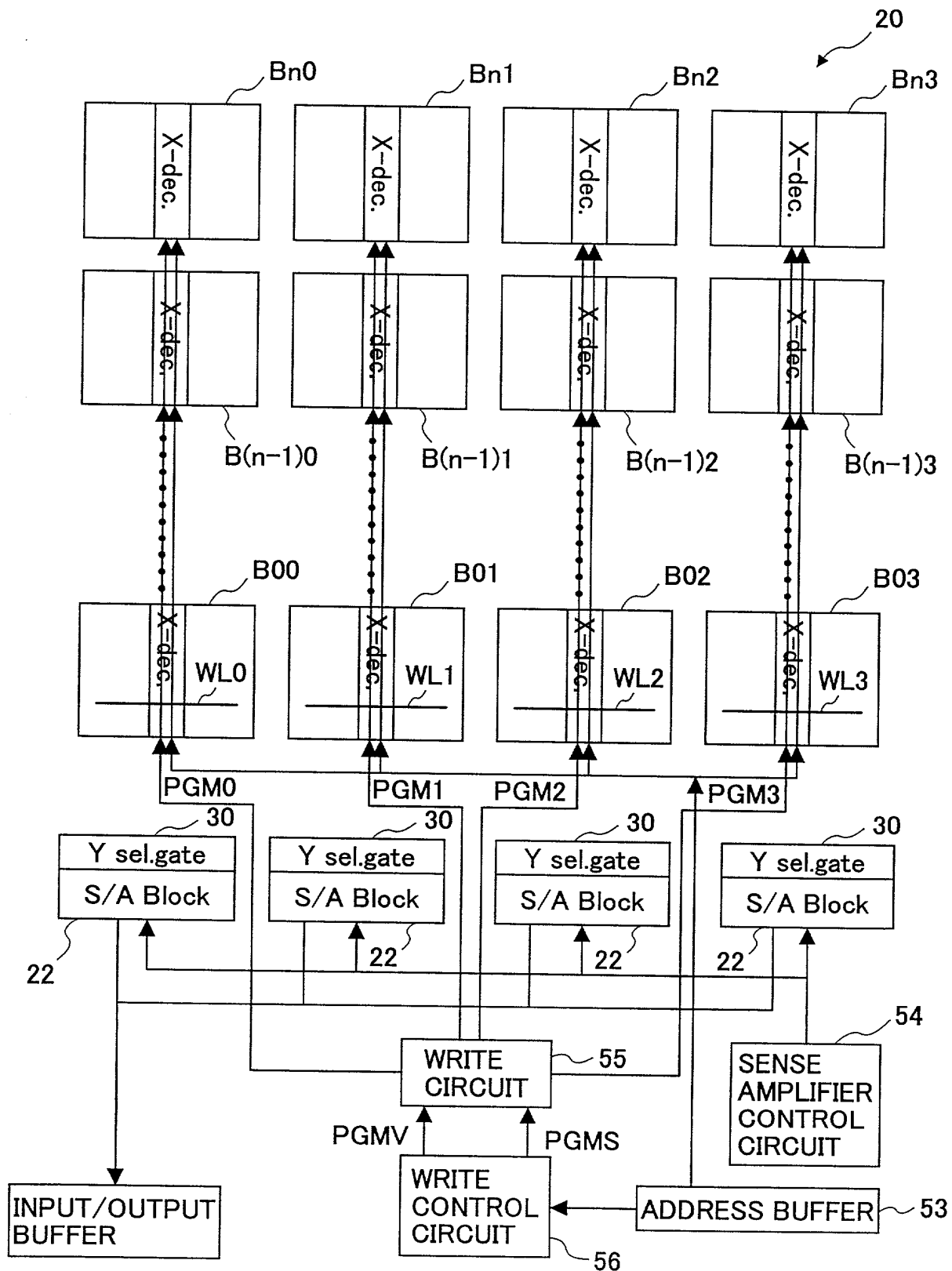


FIG. 8

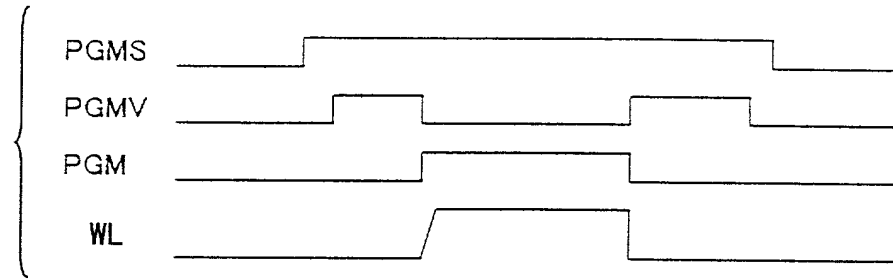




FIG. 9 is a timing diagram showing the relationship between the various signals during a programming operation. The signals are: PGMS, PGMV, PGM0, PGM1, PGM2, PGM3, WL0, WL1, WL2, and WL3. The diagram shows that PGM0, PGM1, PGM2, and PGM3 are active during the programming operation, while WL0, WL1, WL2, and WL3 are active during the erase operation. PGMS is active during both programming and erase operations.

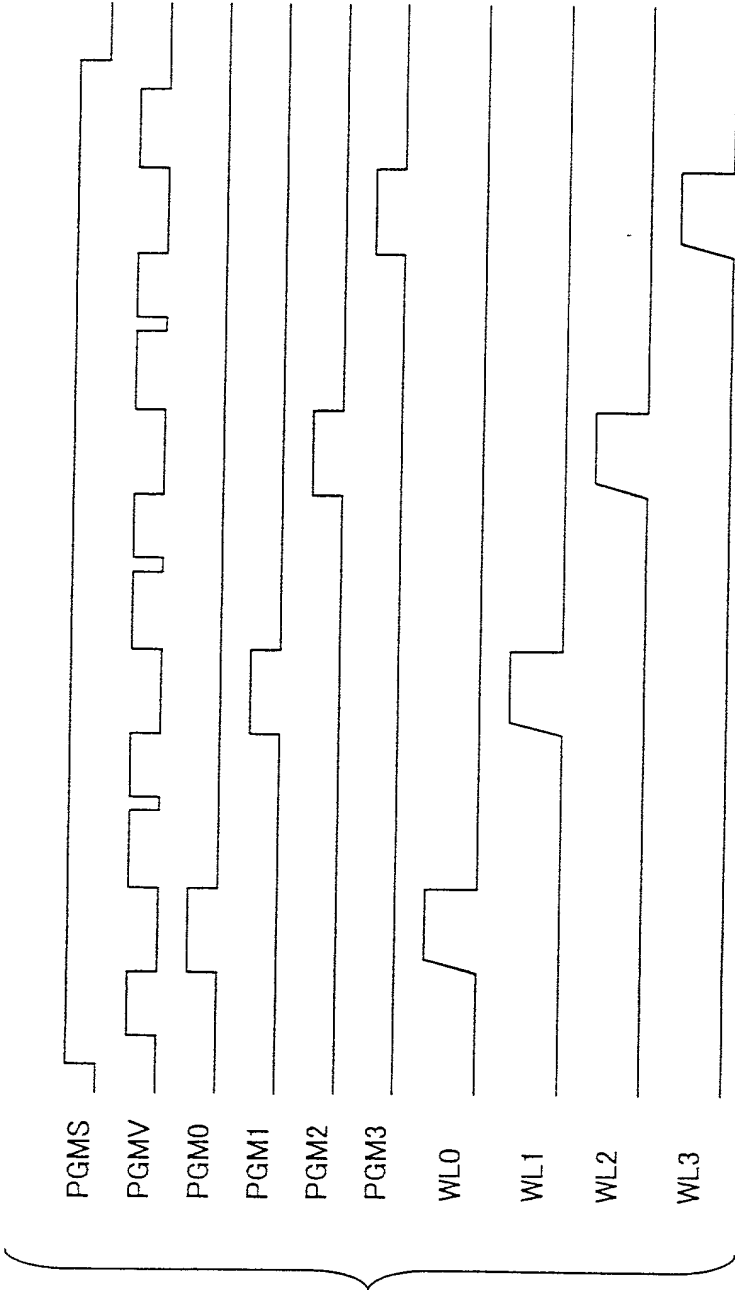


FIG. 9